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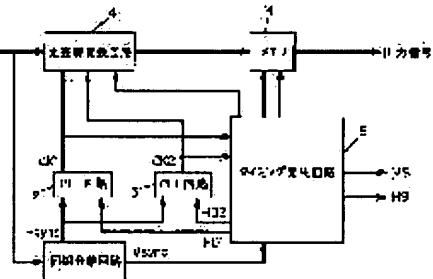
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(54) SCANNING LINE CONVERTER

(57) Abstract:

PROBLEM TO BE SOLVED: To convert a scanning line without missing an image in a display screen by selecting a horizontal synchronizing signal having plural horizontal frequencies within a frame.

SOLUTION: An input signal is given to a synchronizing separator circuit 1, which outputs a horizontal synchronizing signal and a vertical synchronizing signal separately. A PLL circuit 2 recovers a clock 1 based on the horizontal synchronizing signal Hsync and the frequency depends on a counter in a timing generating circuit 5. A PLL circuit 3 generates a clock 2 by other frequency decided by a counter in the timing generating circuit 5 and provides the output to a scanning line conversion circuit 4. The horizontal synchronizing signal of the timing generating circuit 5 including plural horizontal counters is switched in a frame to convert a scanning line without missing in the display screen.



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